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**REPLY UNDER 37 CFR 81.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Chi-Chun Chen, et al.

Serial No.: 10/600,393

Filed: June 20, 2003

For: METHOD OF FORMING DUAL
GATE INSULATOR LAYERS FOR
CMOS APPLICATIONS

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Attorney Docket No.:
2002-0066 / 24061.461


Customer No. 42717

Group Art Unit: 2822

Examiner: Toniae M. Thomas

Confirmation No.: 8529

Mall Stop AF
Commissioner for Patents
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Kelly A. Moreau

Sir:

AMENDMENT UNDER 37 C.F.R. 81.116

IN REPLY TO ADVISORY ACTION

This Amendment is submitted in reply to the Advisory Action mailed on May 23, 2005, and in reply to the Office Action mailed on March 25, 2005. (Please see the pages that follow).